

1

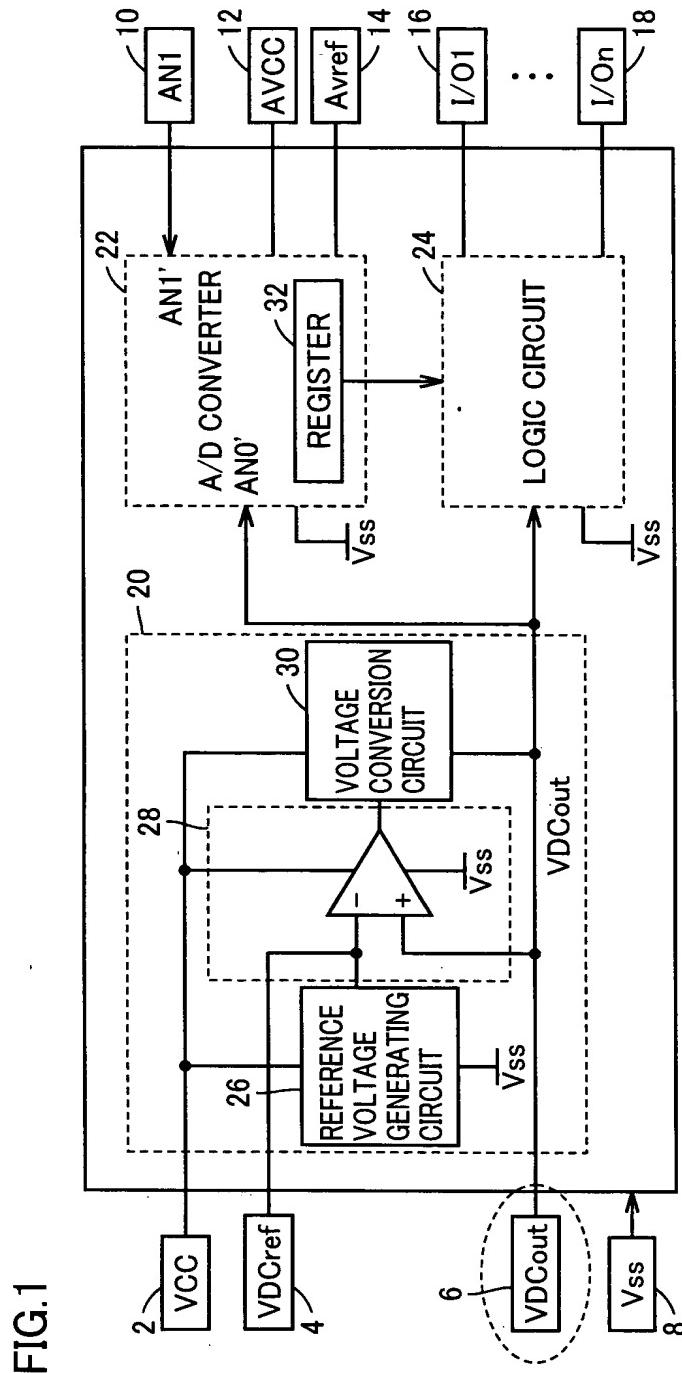


FIG.1

50

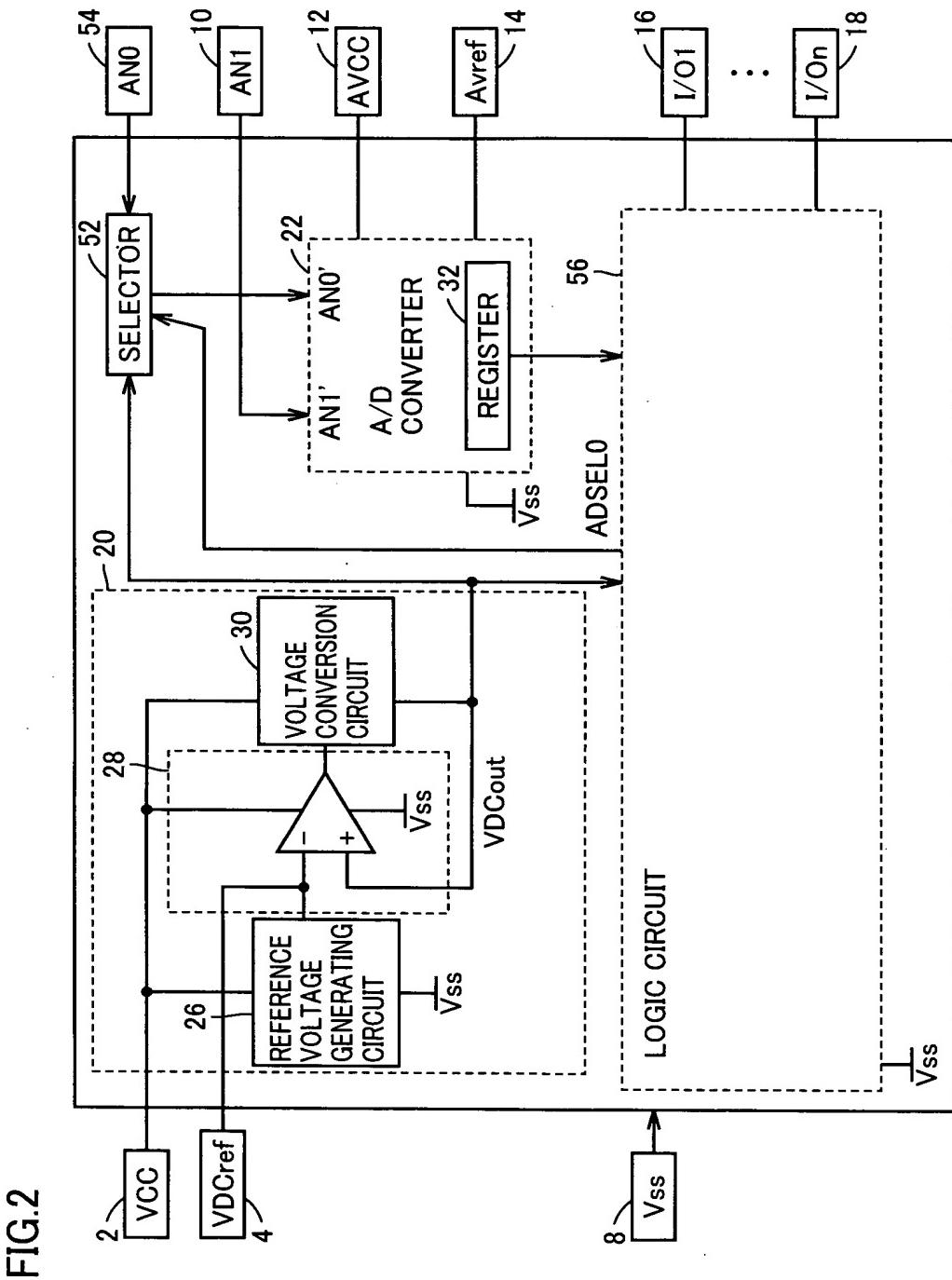


FIG.2

60

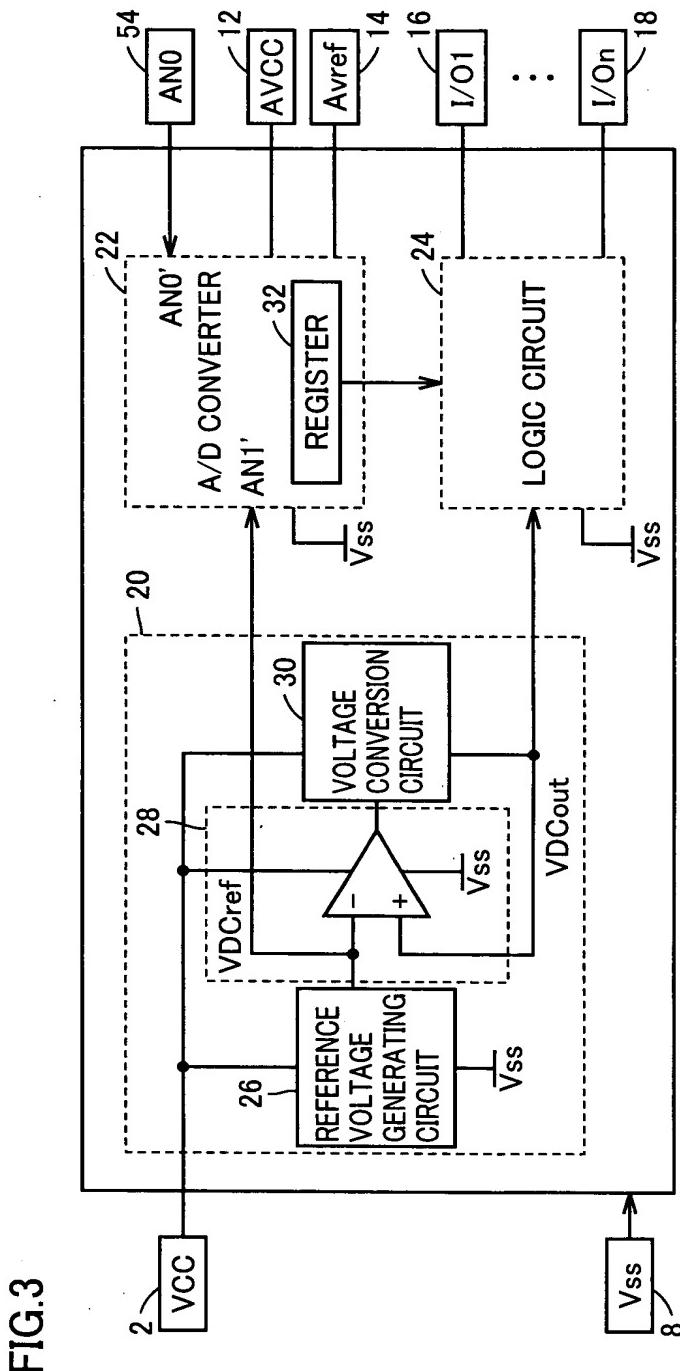
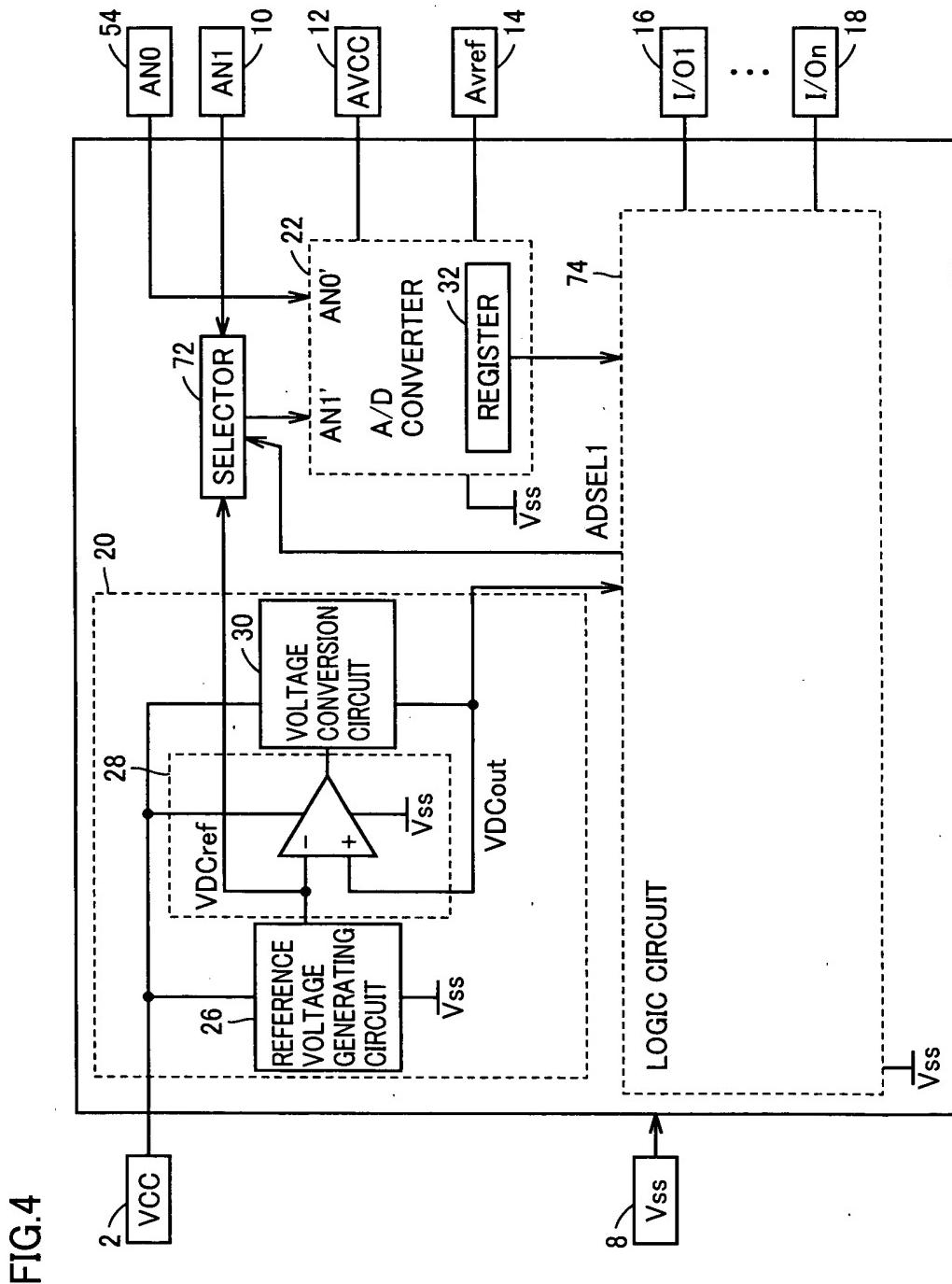


FIG.3

70



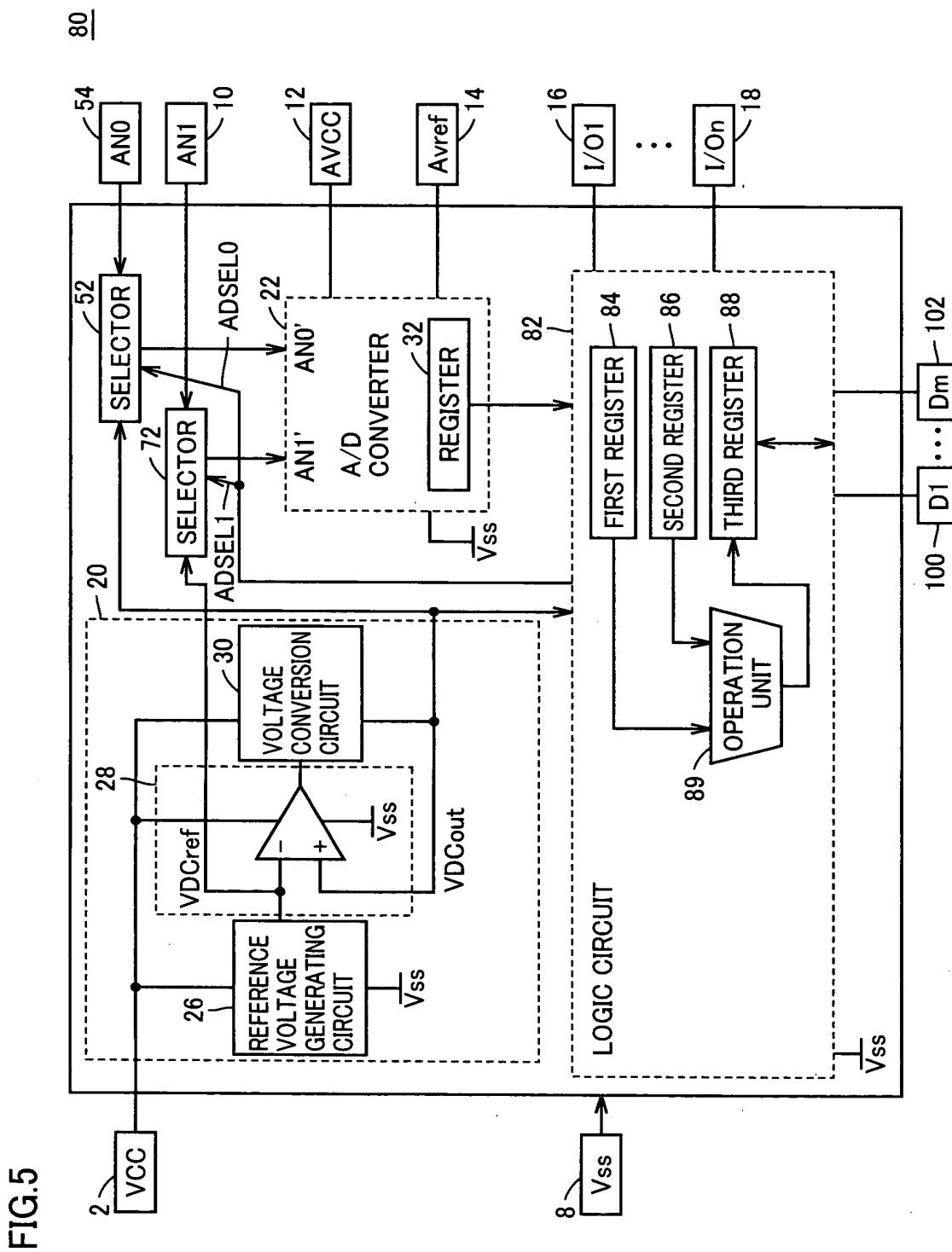


FIG.5

FIG.6

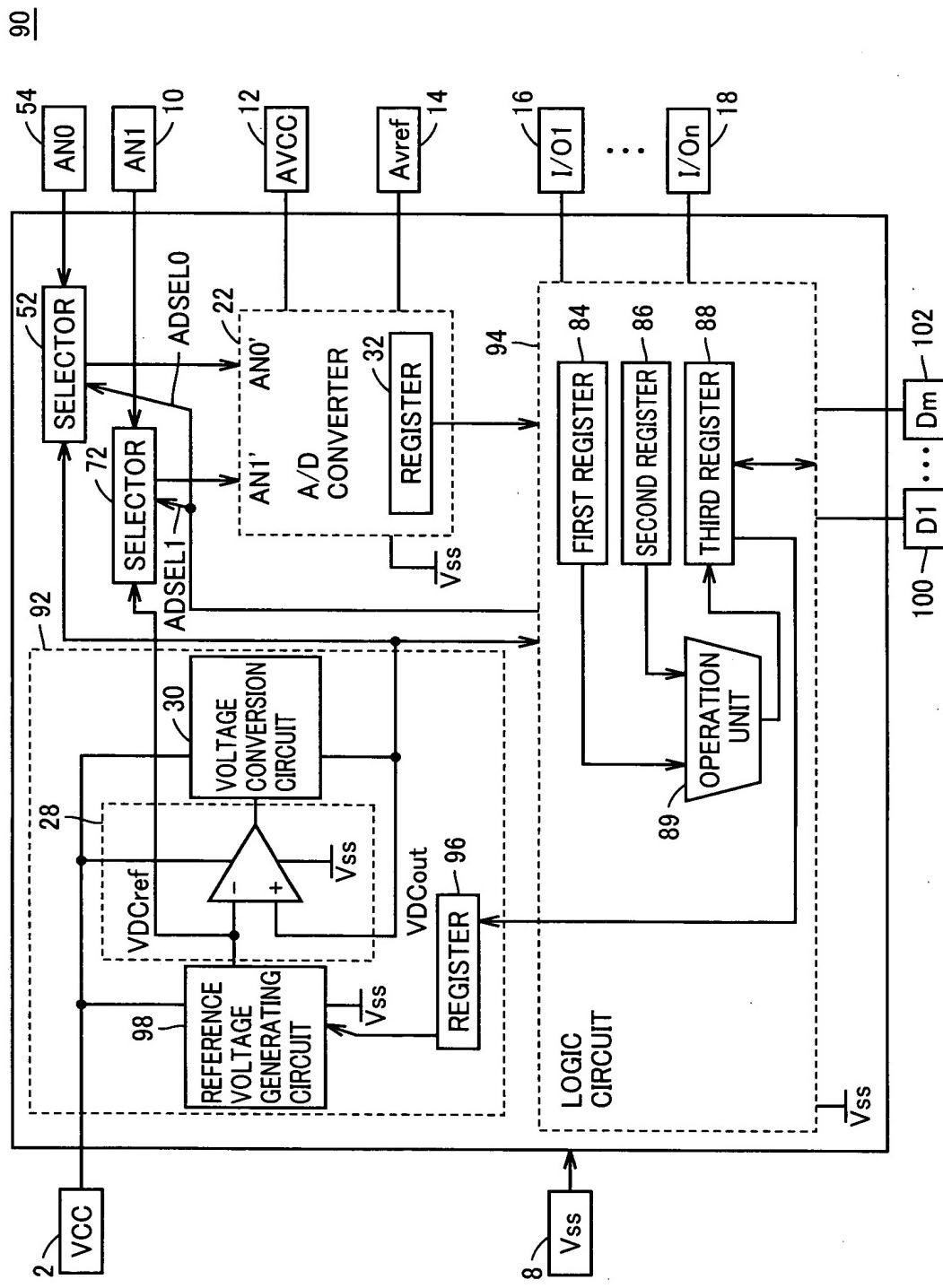


FIG. 7

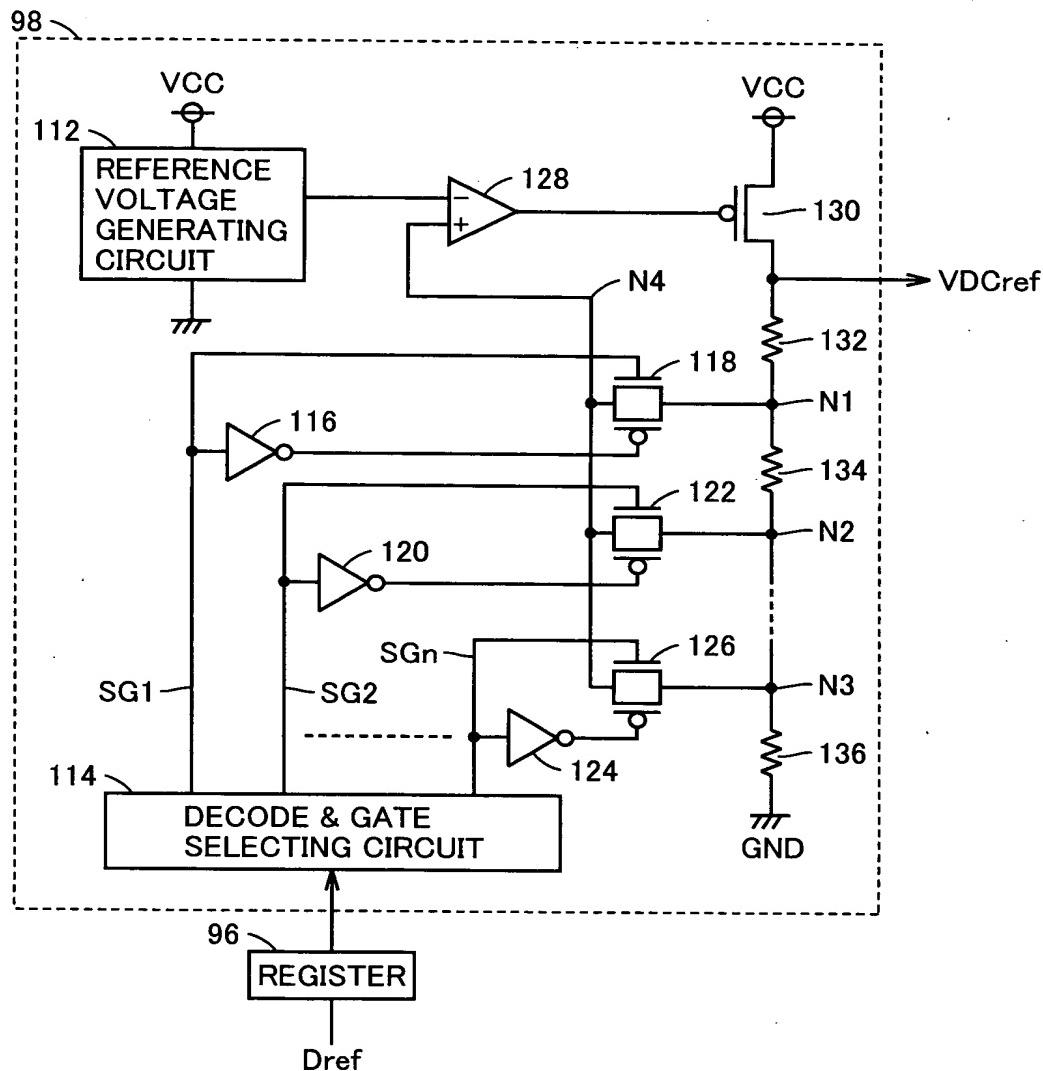


FIG.8

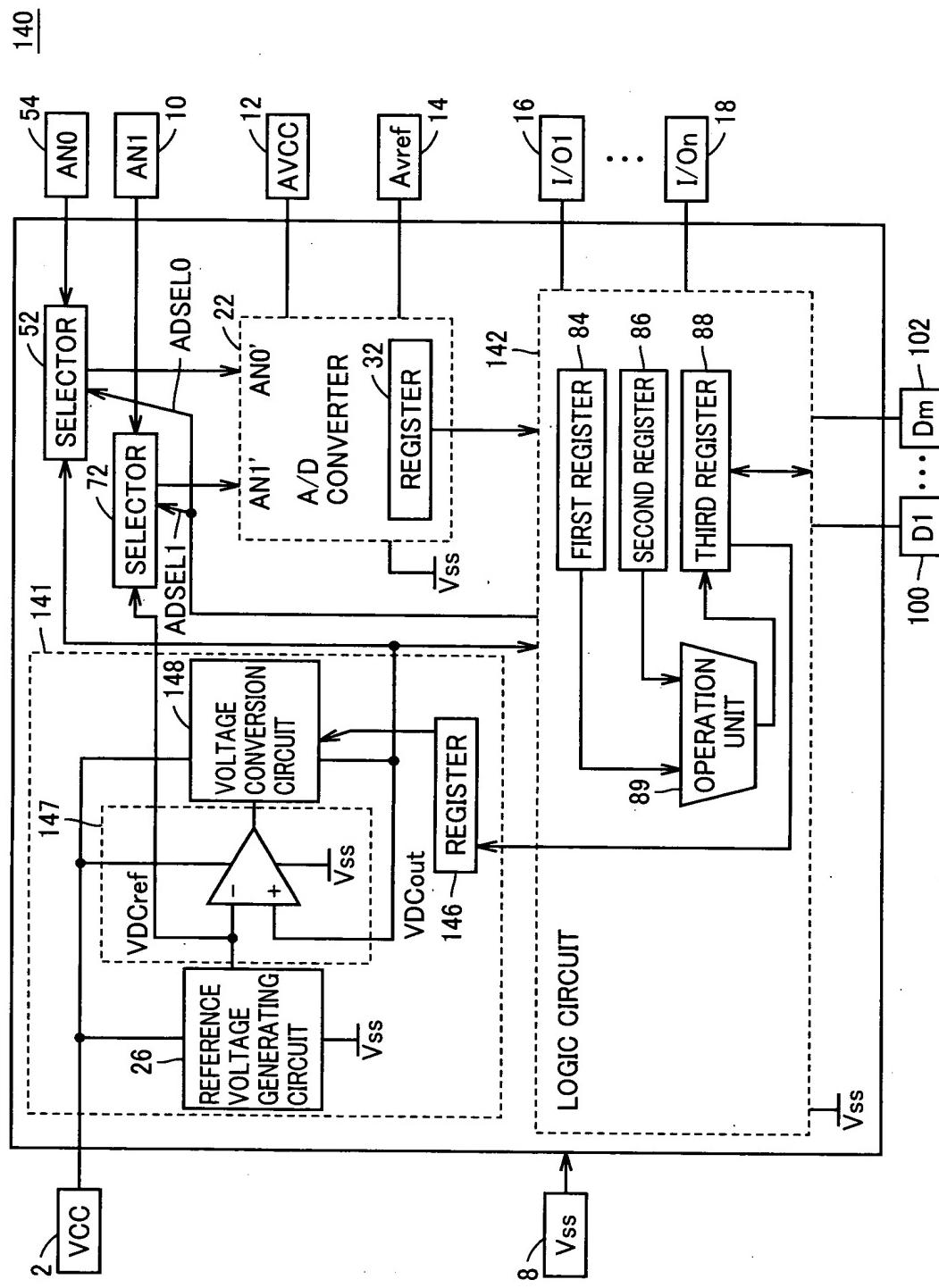


FIG.9

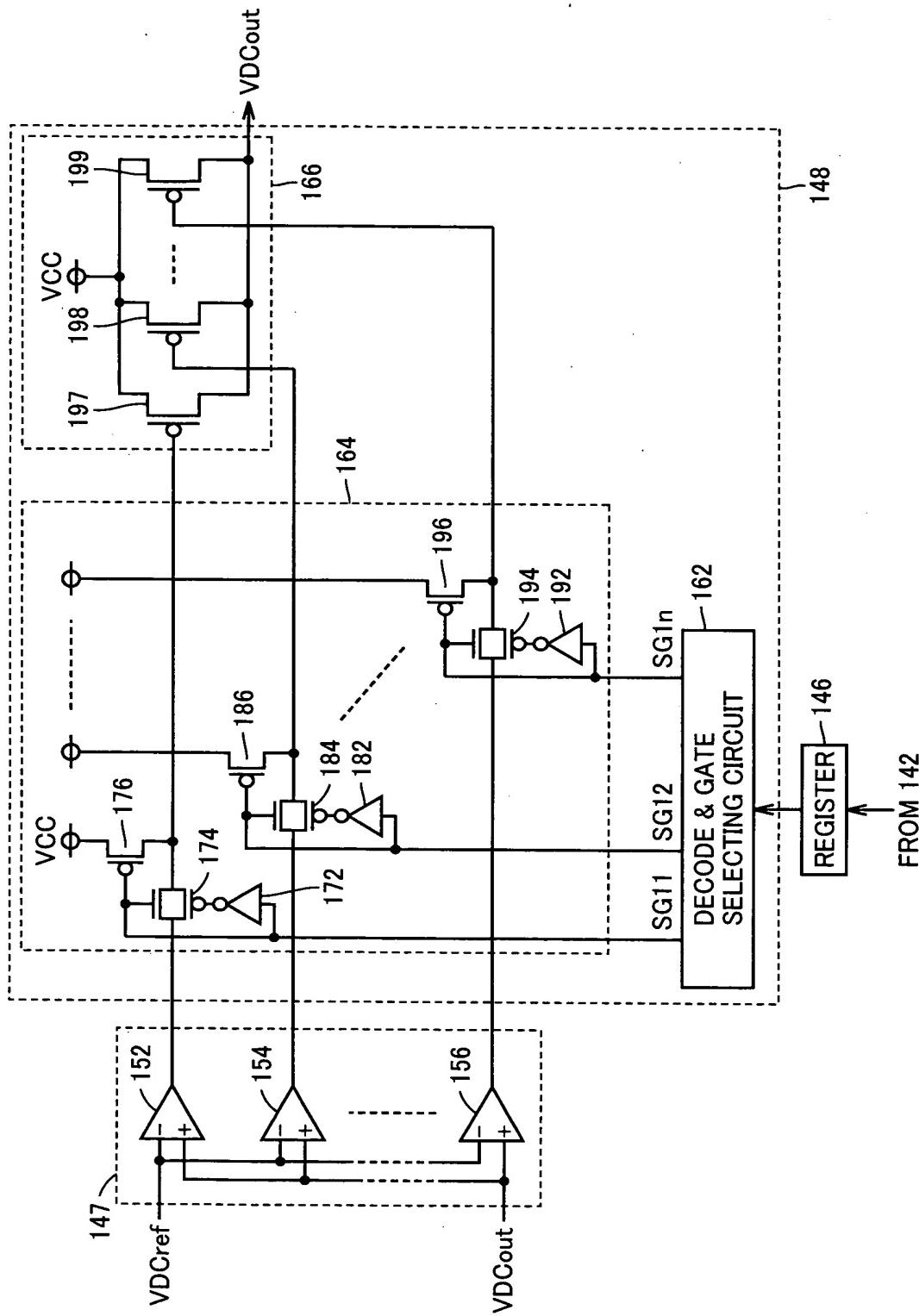
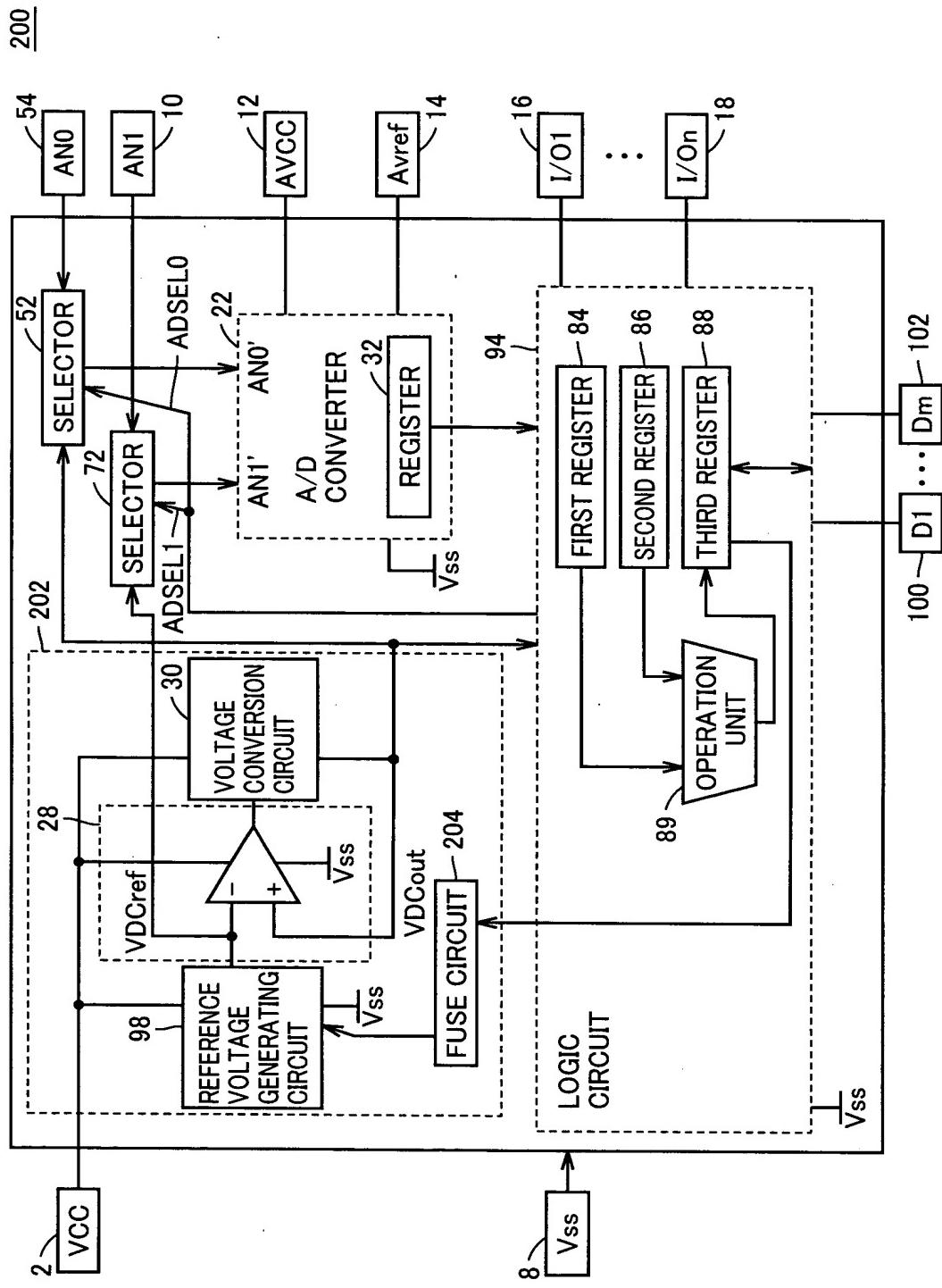


FIG.10



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TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT IN
WHICH VOLTAGE DOWN CONVERTER OUTPUT CAN
BE OBSERVED AS DIGITAL VALUE AND VOLTAGE
DOWN CONVERTER OUTPUT VOLTAGE ...
INVENTOR(S): TADAYOSHI NAKANO ET AL.
ATTORNEY DOCKET NO.: 009683-501 SHEET 11 OF 24

FIG.11

204

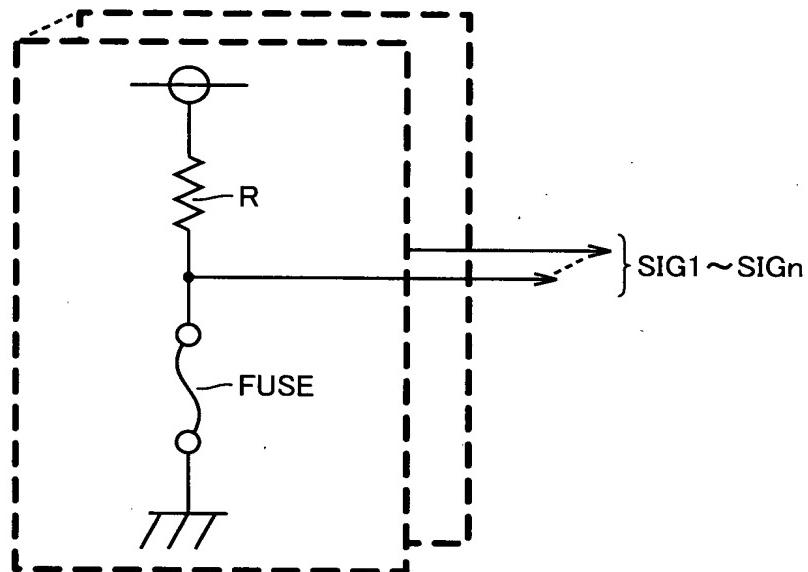


FIG.12

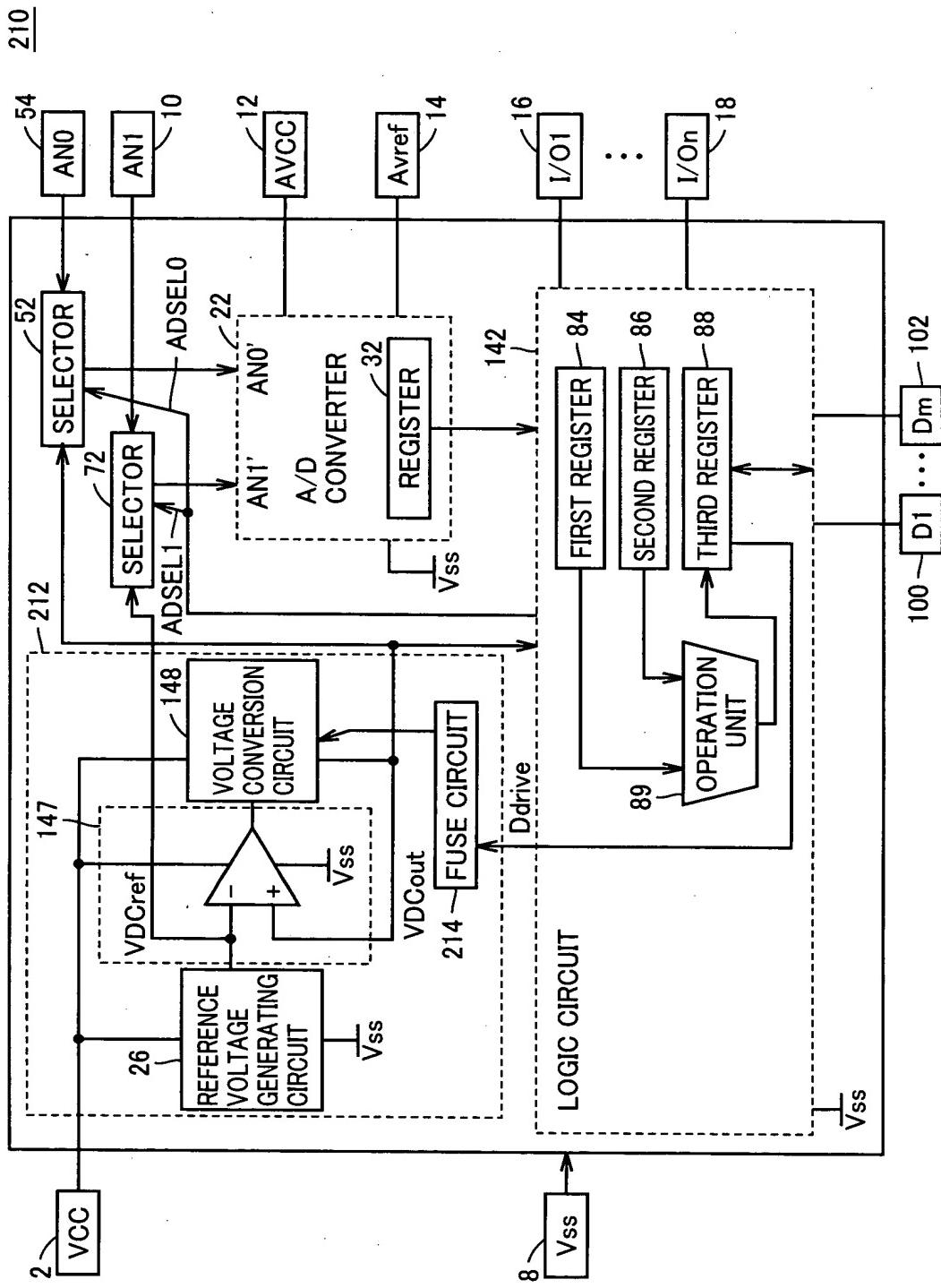


FIG.13

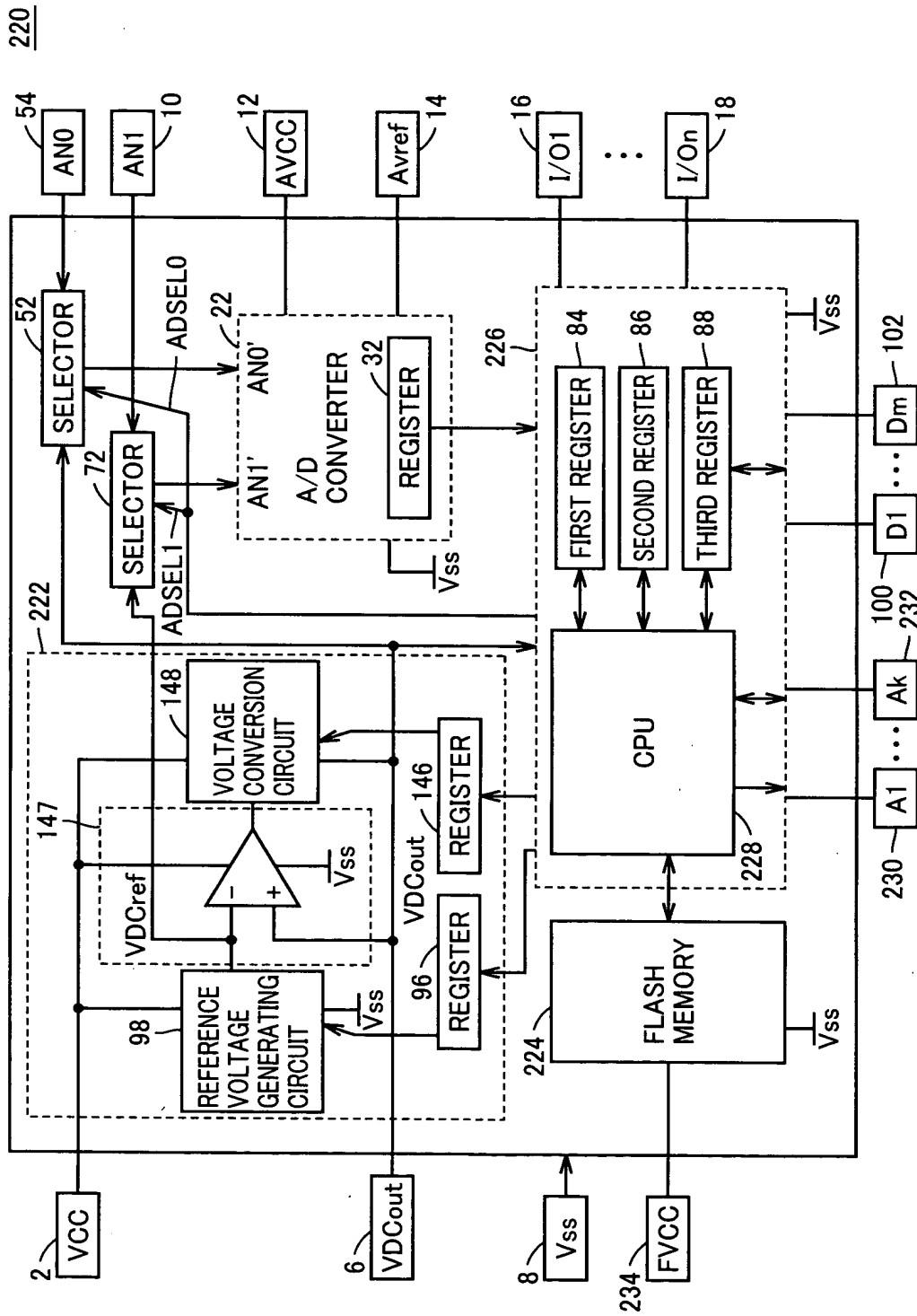


FIG.14

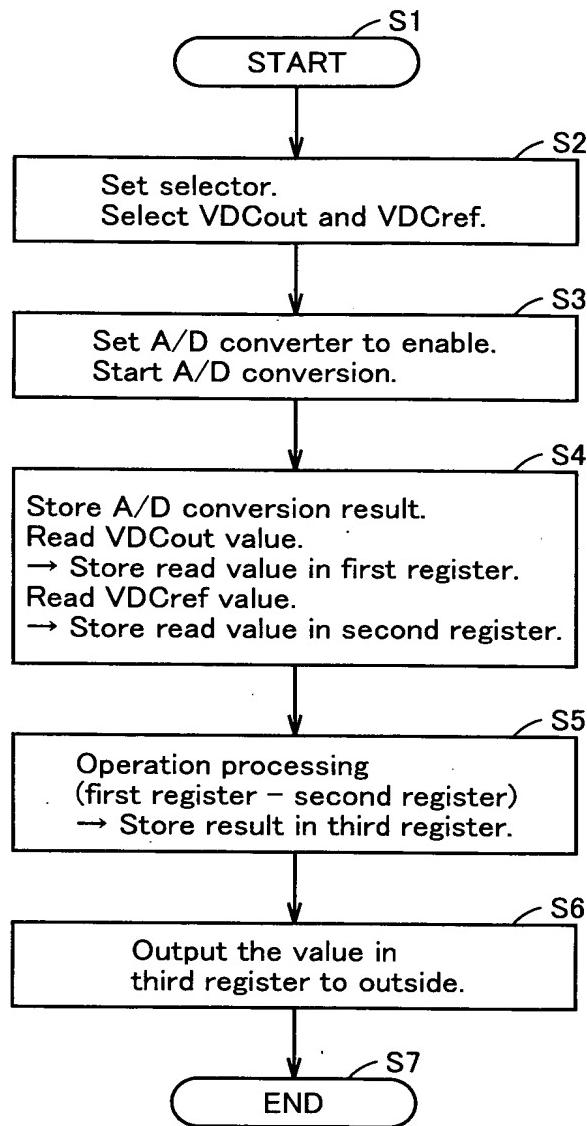


FIG.15

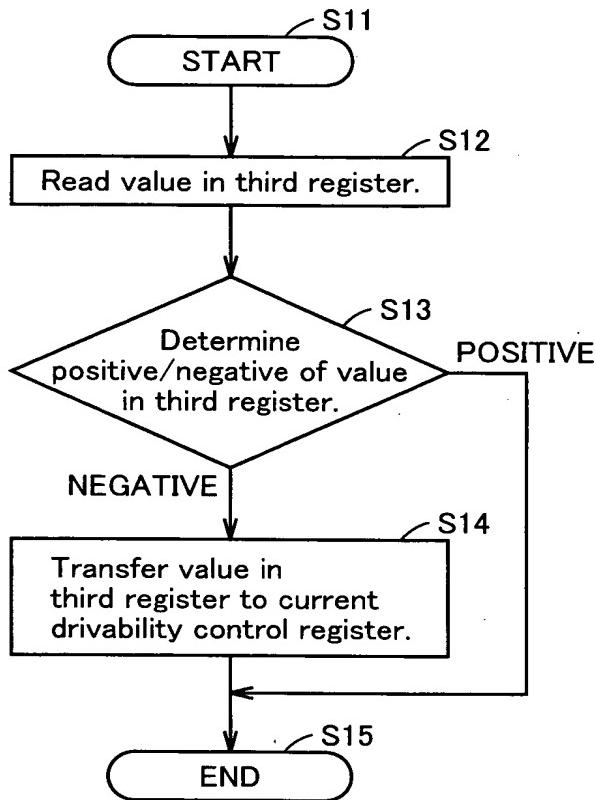


FIG.16

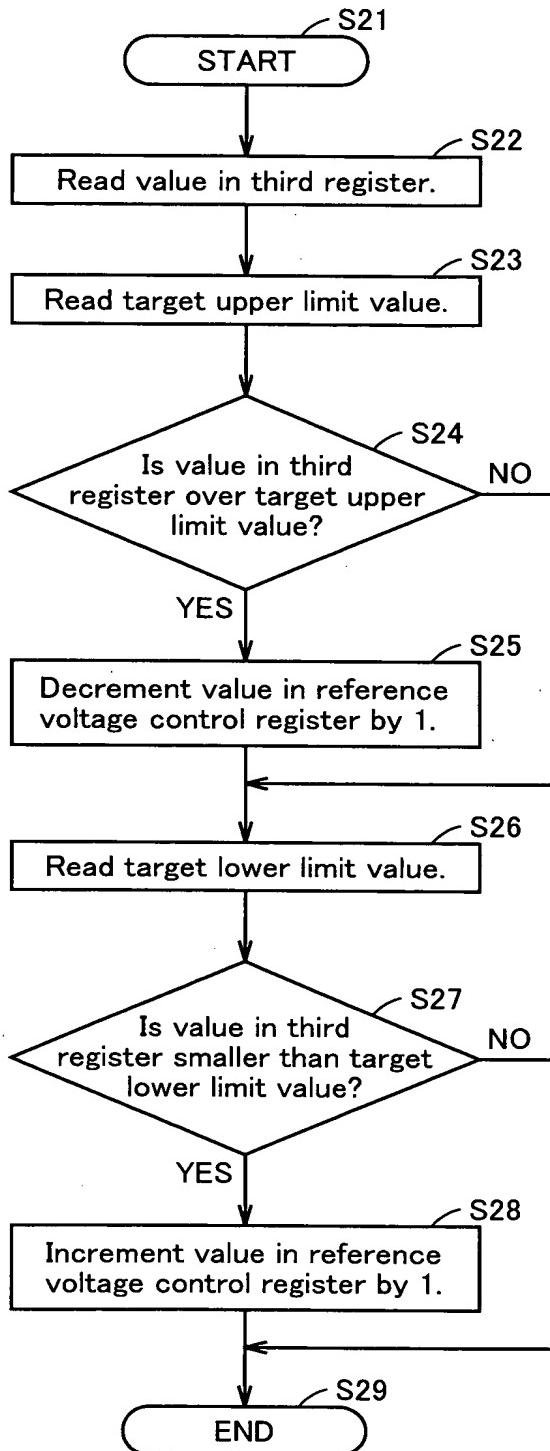


FIG. 17

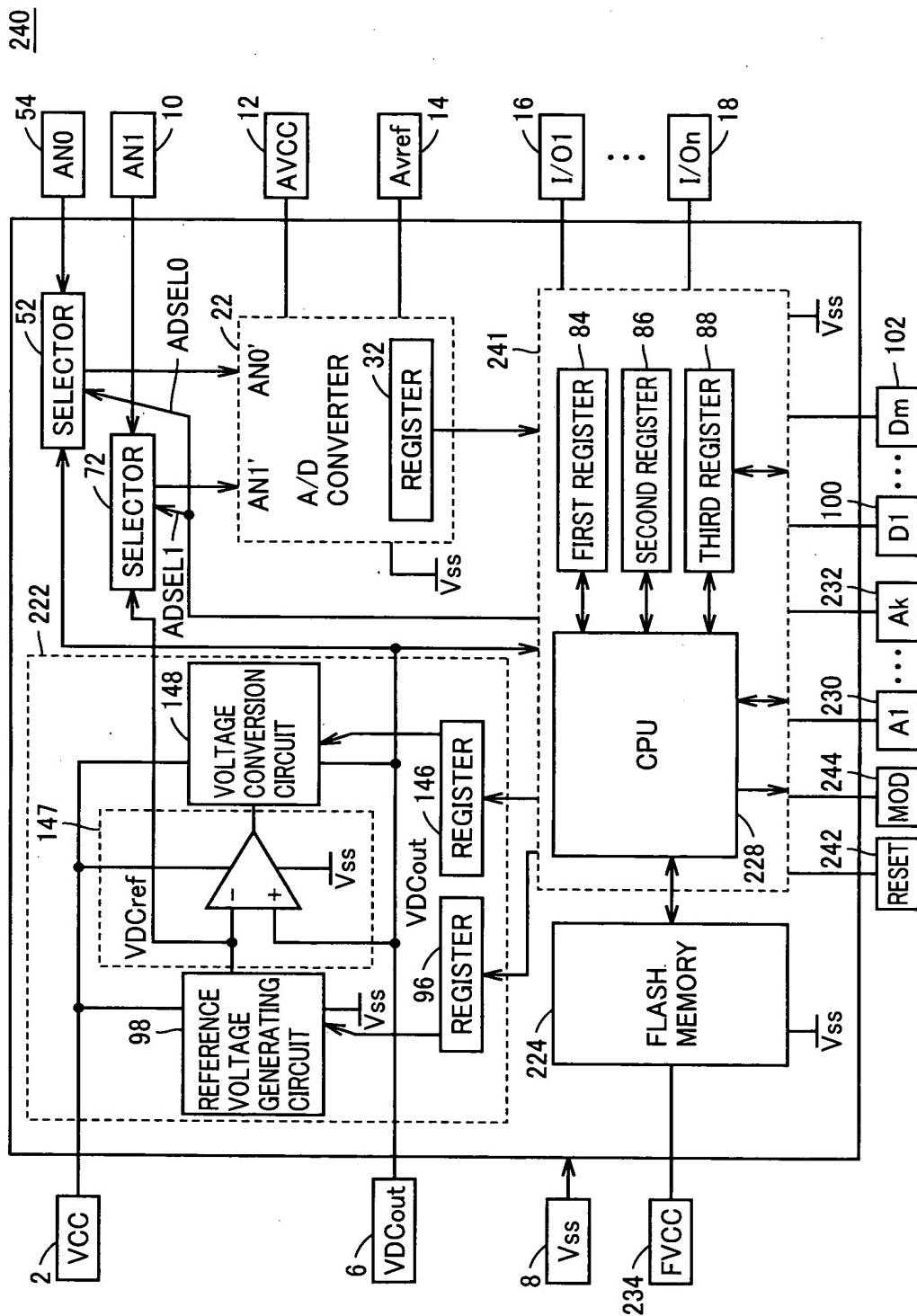


FIG. 18

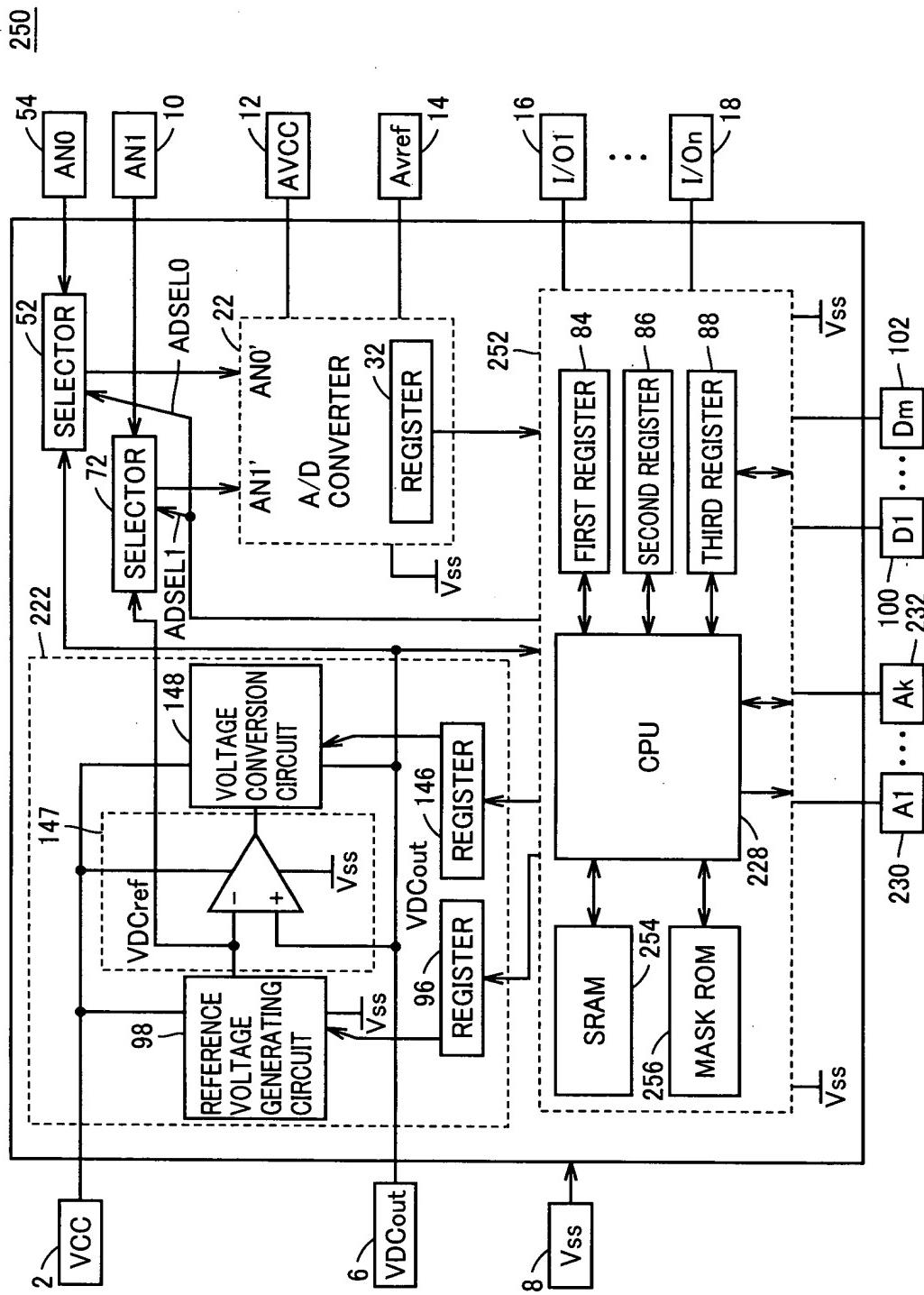


FIG. 19

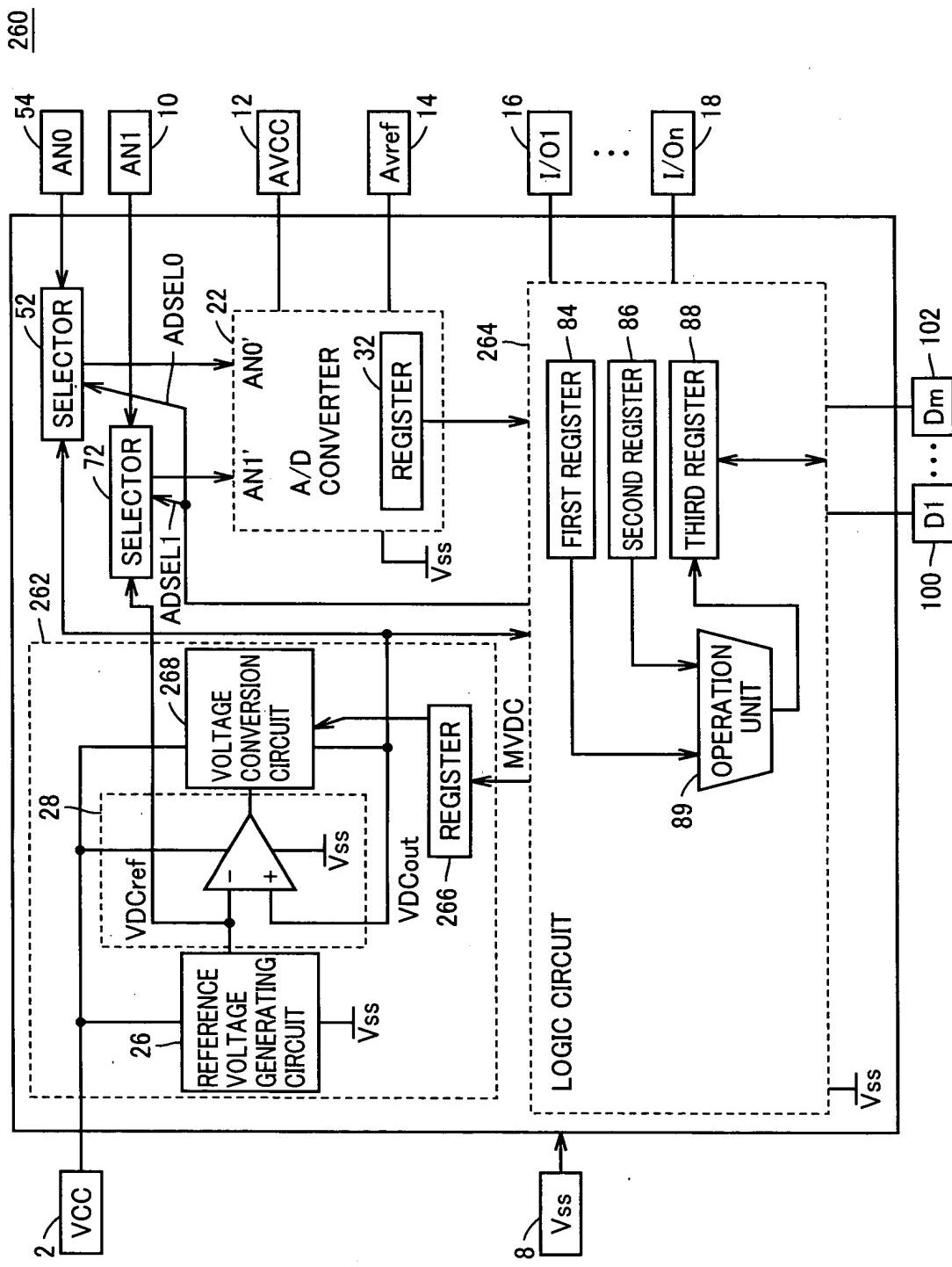


FIG.20

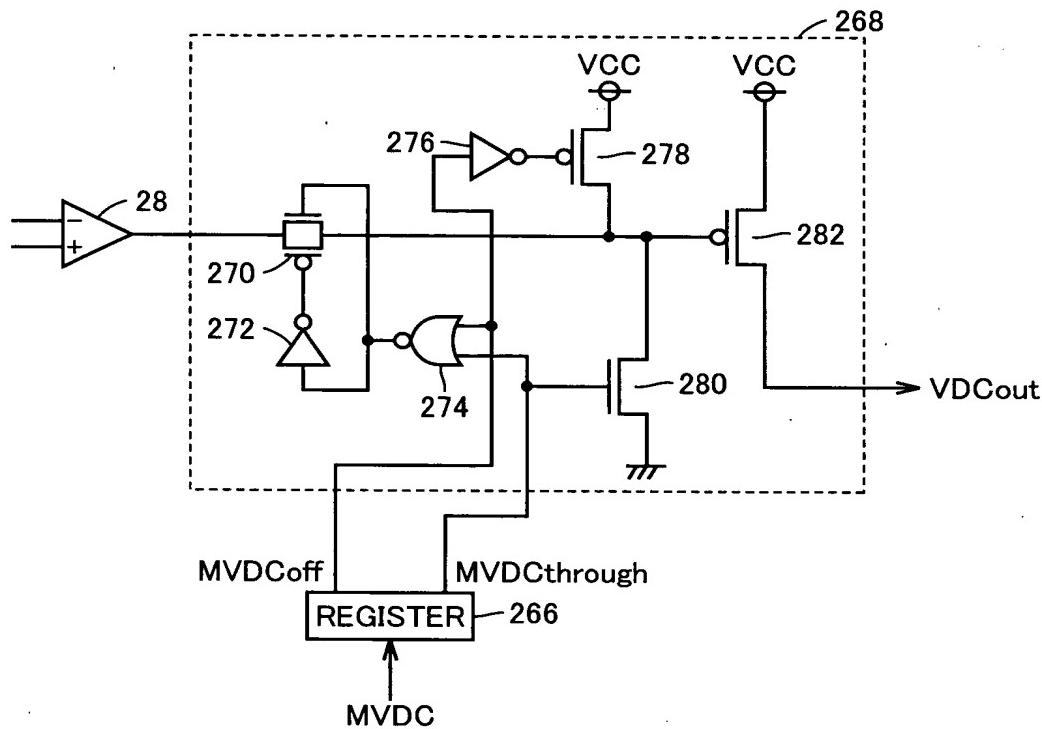
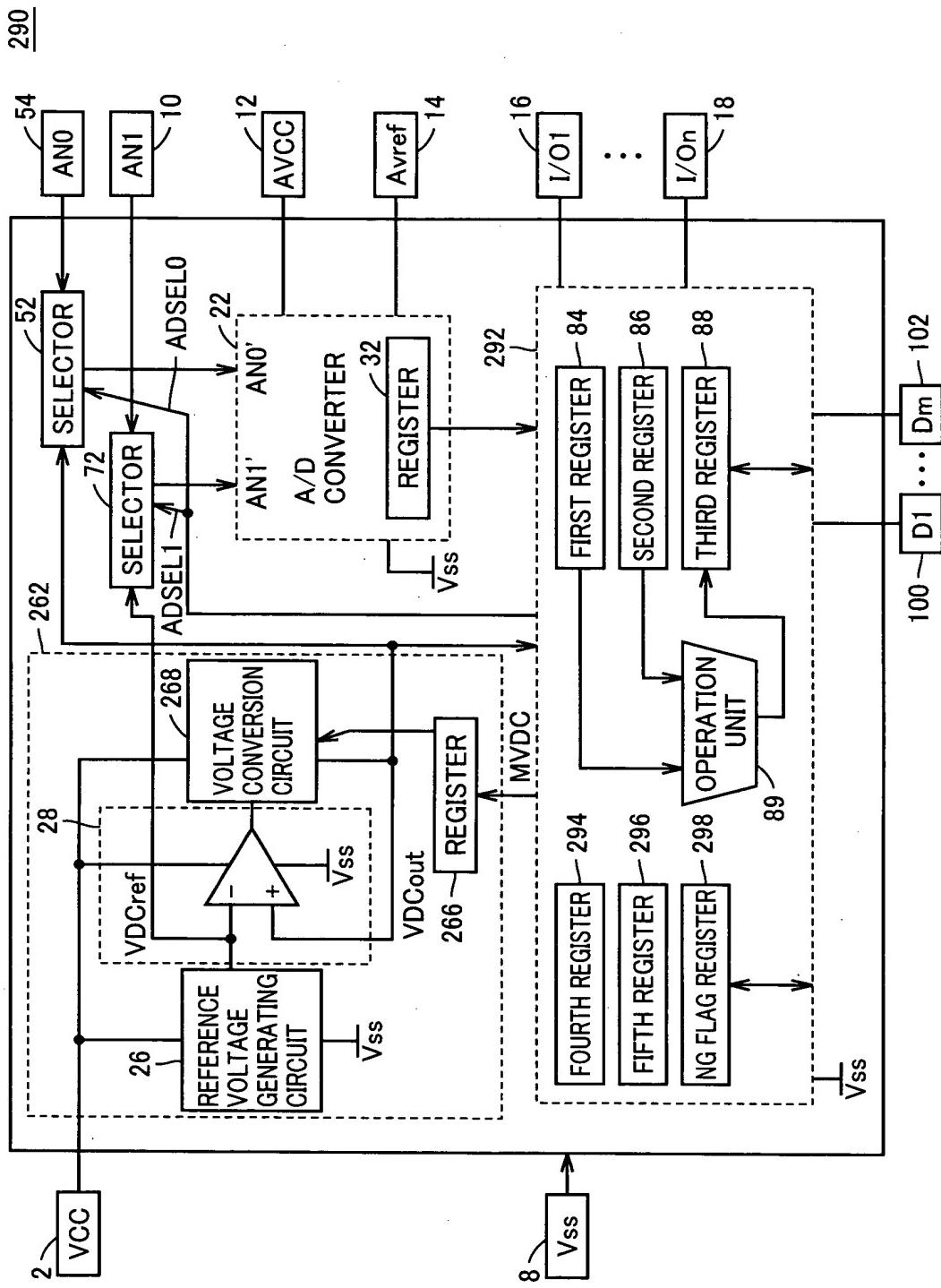
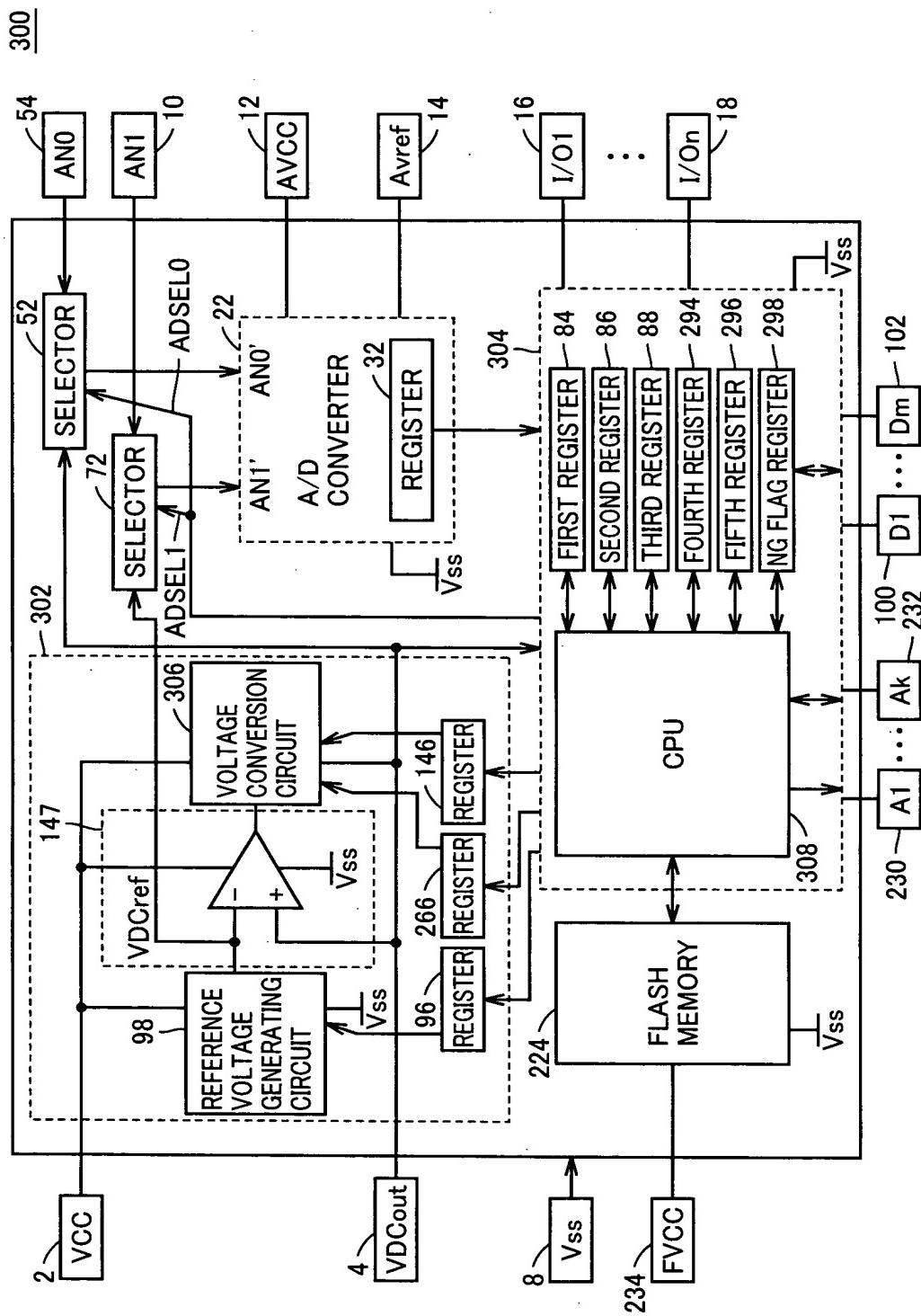


FIG.21



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TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT IN WHICH VOLTAGE DOWN CONVERTER OUTPUT CAN BE OBSERVED AS DIGITAL VALUE AND VOLTAGE DOWN CONVERTER OUTPUT VOLTAGE . . .
INVENTOR(S): TADAYOSHI NAKANO ET AL.
ATTORNEY DOCKET NO.: 009683-501 **SHEET 22 OF 24**

FIG.22



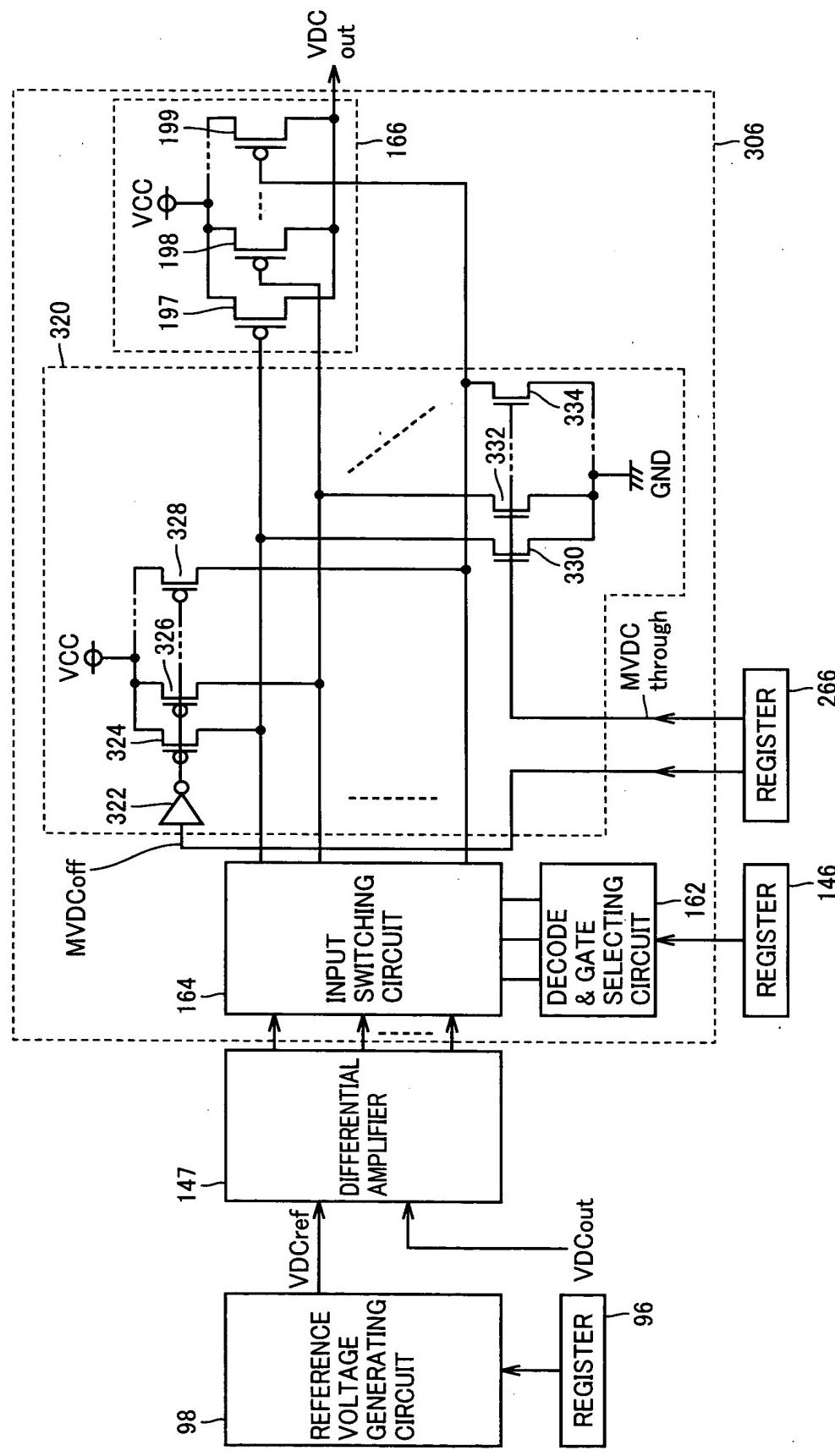


FIG.23

FIG.24

